

[illegible]

ALL-TRACK ERASION STANDBY

FIG.2

		SW2	SW3	SW4	SW5
FIELD REPRODUCTION	ODD FIELD	CONNECT HEAD 3-1 TO REPRODUCTION AMP. 6	INTERMEDIATE POSITION	OUTPUT FROM DEMODULATION CKT 9	CONNECT MONITOR 13 TO SWITCH SW4
	EVEN FIELD			OUTPUT VIA 1/2 H DELAY CKT 10	
FRAME REPRODUCTION	ODD FIELD	CONNECT HEAD 3-1 TO REPRODUCTION AMP. 6	INTERMEDIATE POSITION	OUTPUT FROM DEMODULATION CKT 9	
	EVEN FIELD	INTERMEDIATE POSITION	CONNECT HEAD 3-2 TO REPRODUCTION AMP. 6		
FIELD RECORDING	ODD FIELD	CONNECT HEAD 3-1 TO RECORDING AMP. 16	INTERMEDIATE POSITION	<div></div>	CONNECT MONITOR 13 TO VIDEO SIGNAL INPUT TERMINAL 18
	EVEN FIELD				
FRAME RECORDING	ODD FIELD	CONNECT HEAD 3-1 TO RECORDING AMP. 16	INTERMEDIATE POSITION		
	EVEN FIELD	INTERMEDIATE POSITION	CONNECT HEAD 3-2 TO RECORDING AMP 16		
WHEN EXECUTING FIELD ERASION		CONNECT HEAD 3-1 TO RECORDING AMP 16	INTERMEDIATE POSITION	<div></div>	<div></div>
WHEN EXECUTING FRAME ERASION		CONNECT HEAD 3-1 TO RECORDING AMP 16	CONNECT HEAD 3-2 TO RECORDING AMP 16	<div></div>	<div></div>

FIG.3

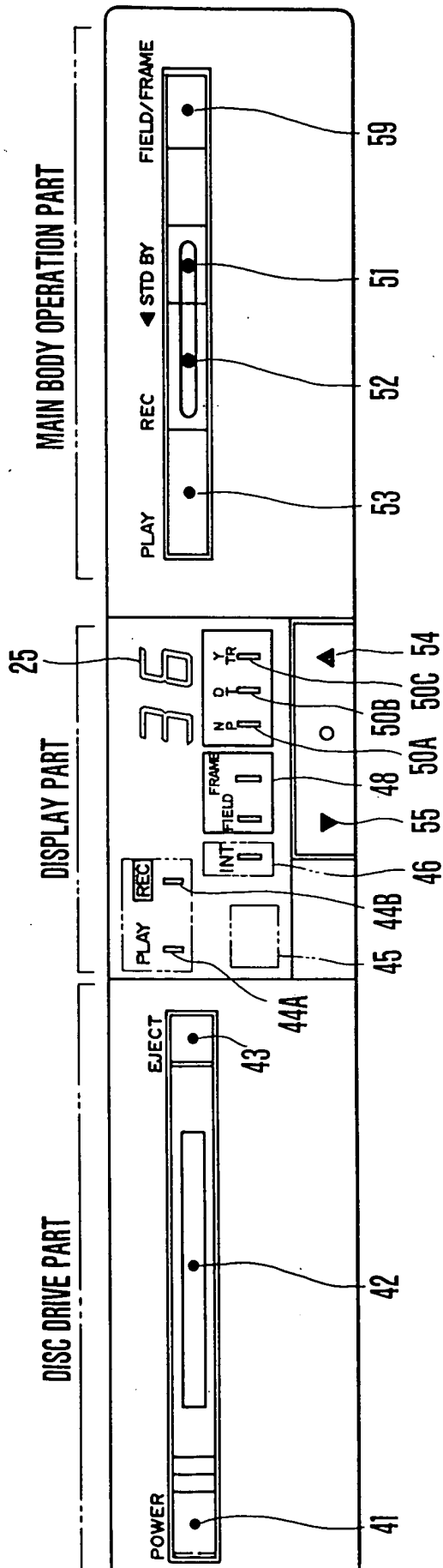
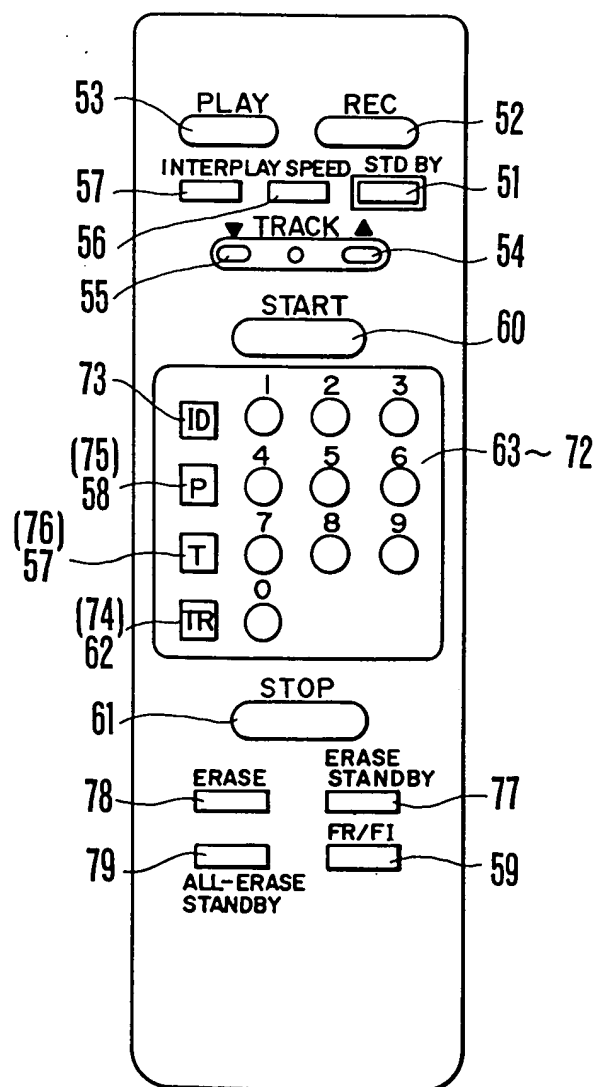
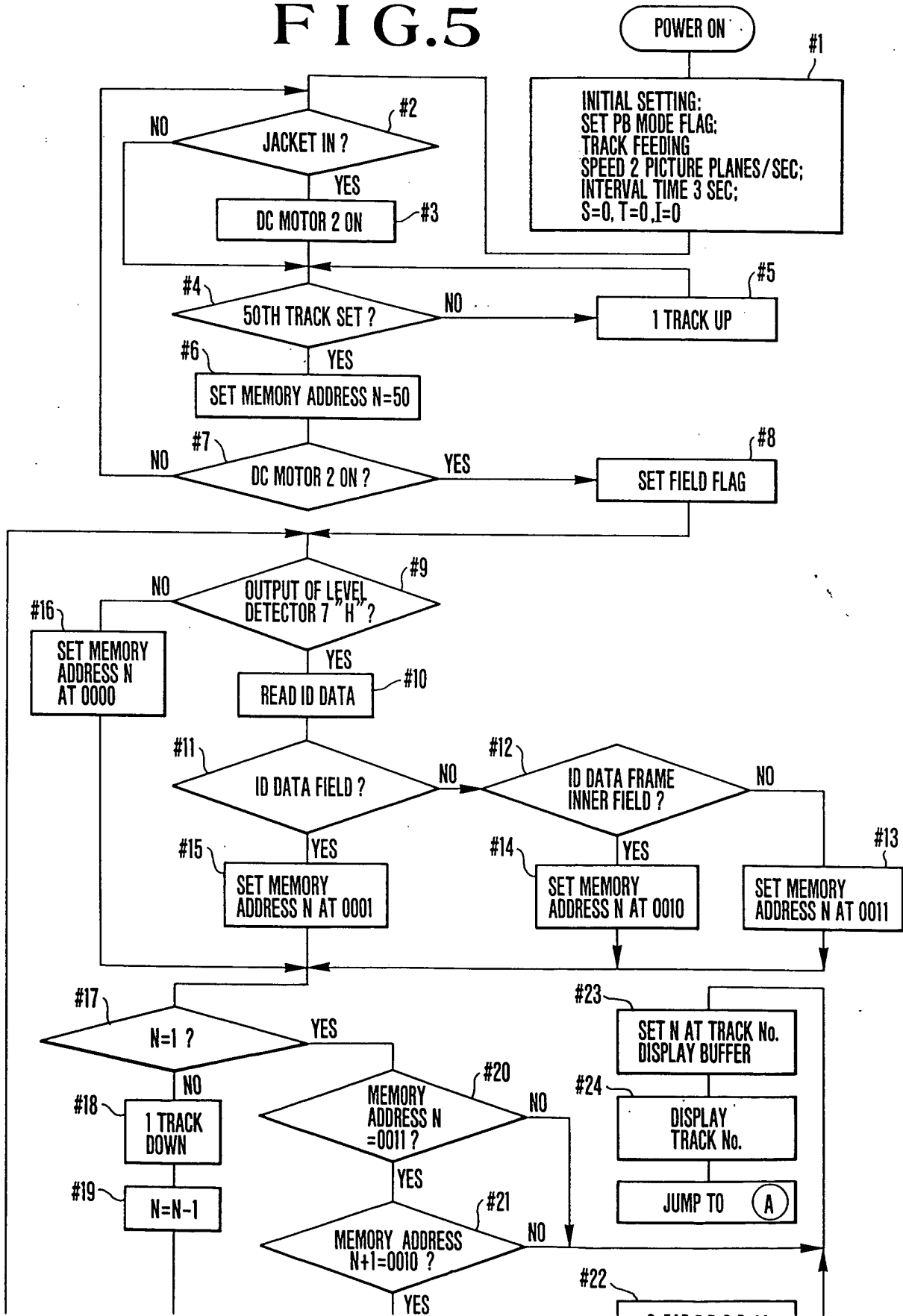


FIG.4



# FIG. 5



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FIG. 6A

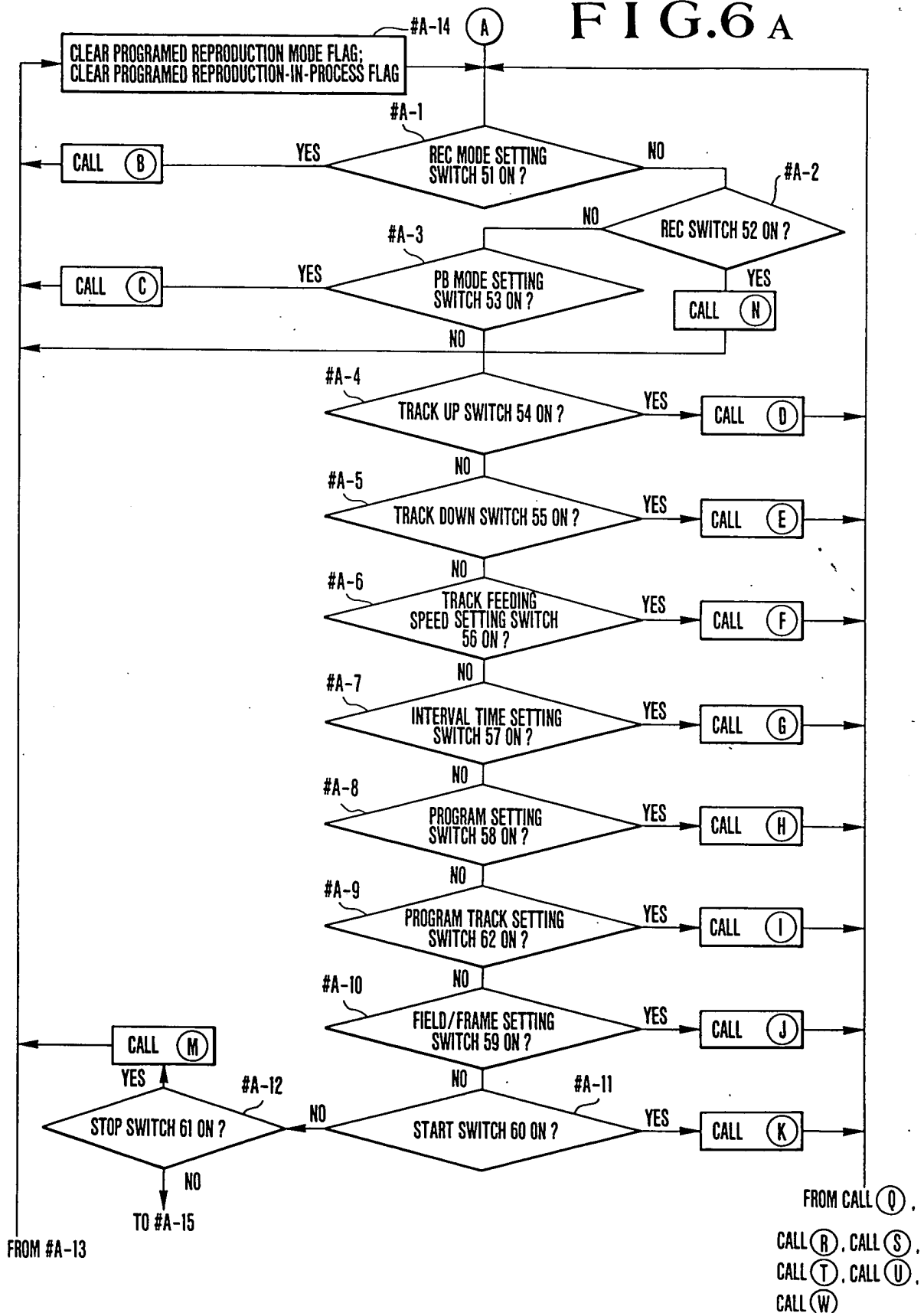


FIG. 6B

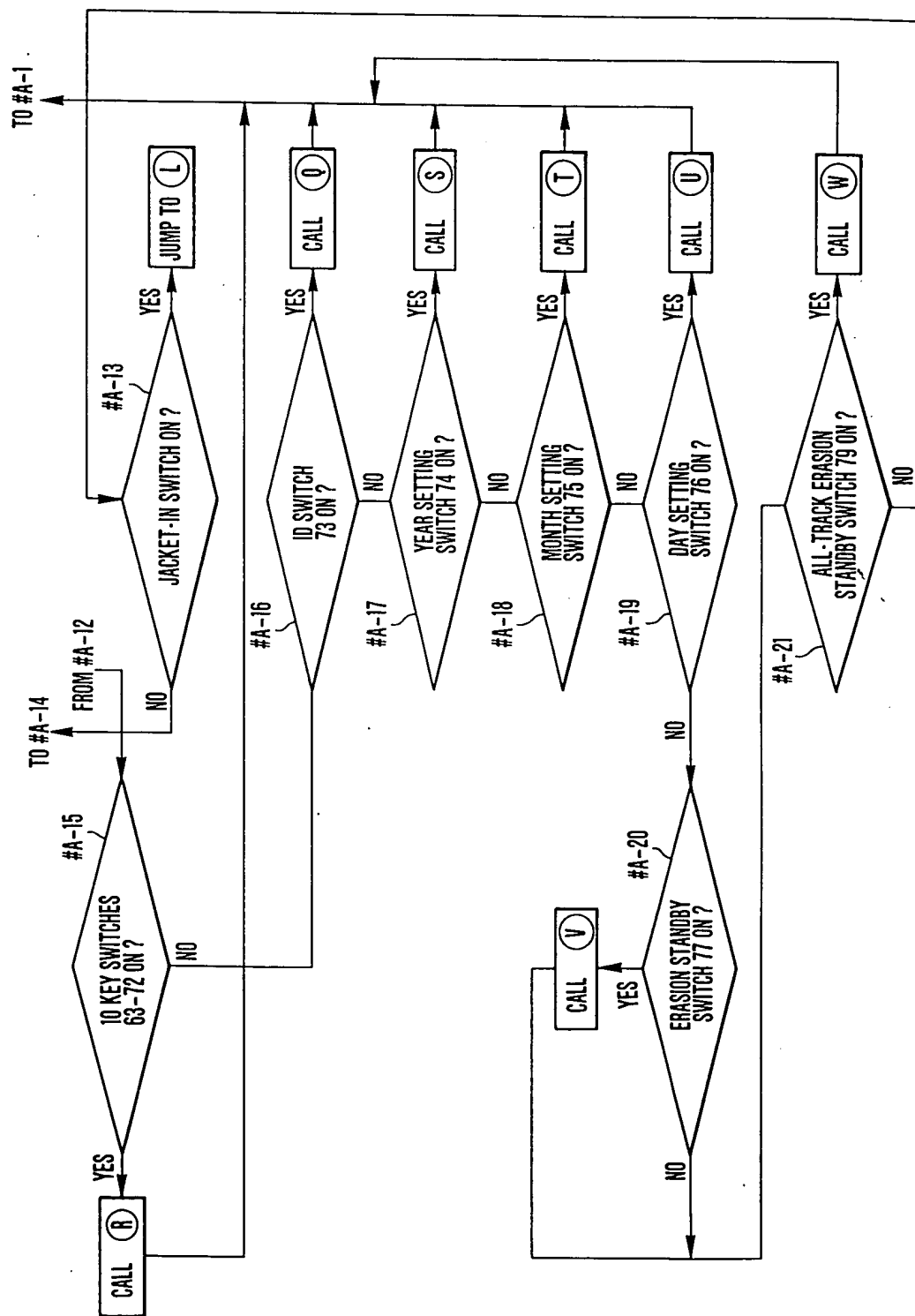
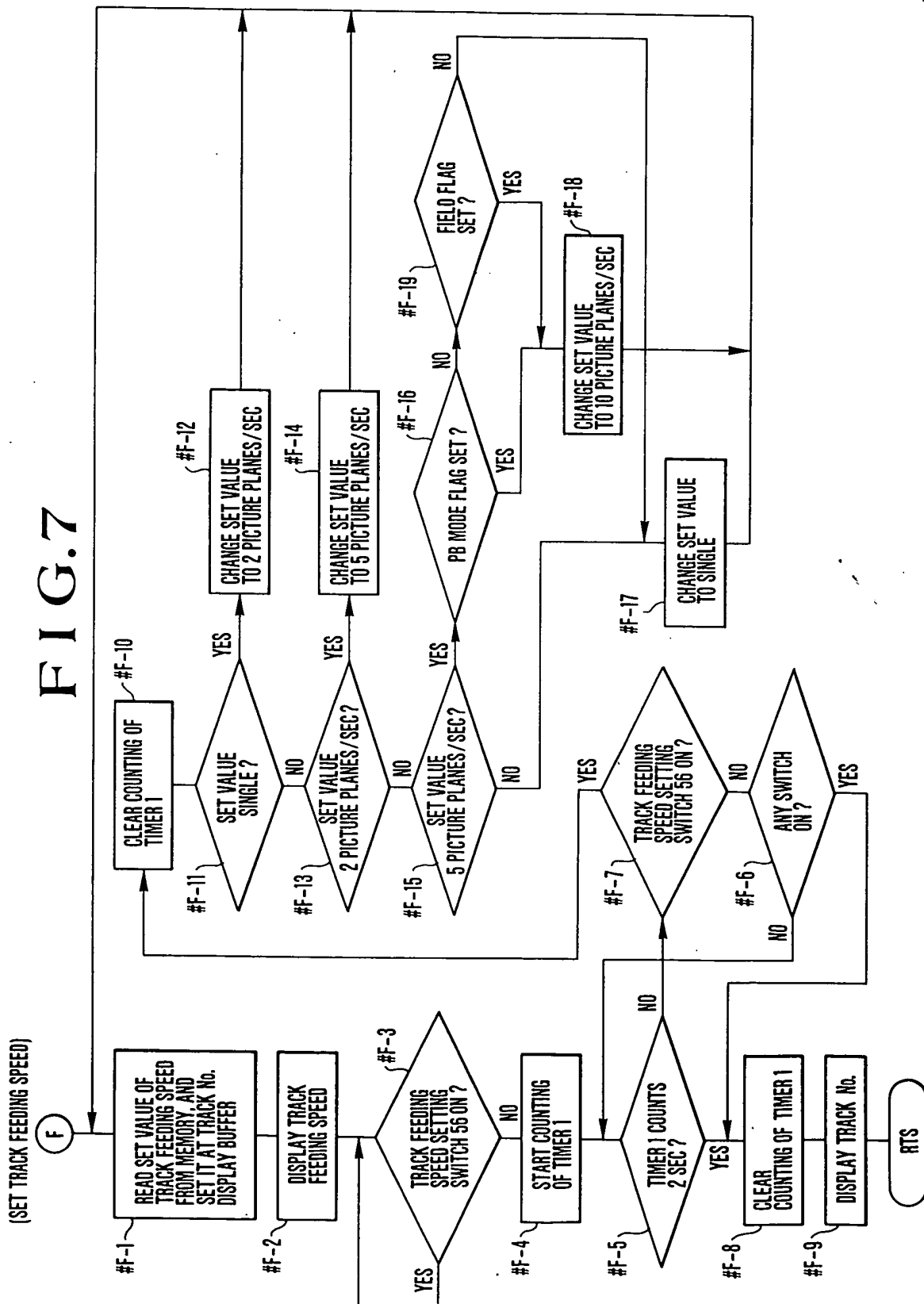
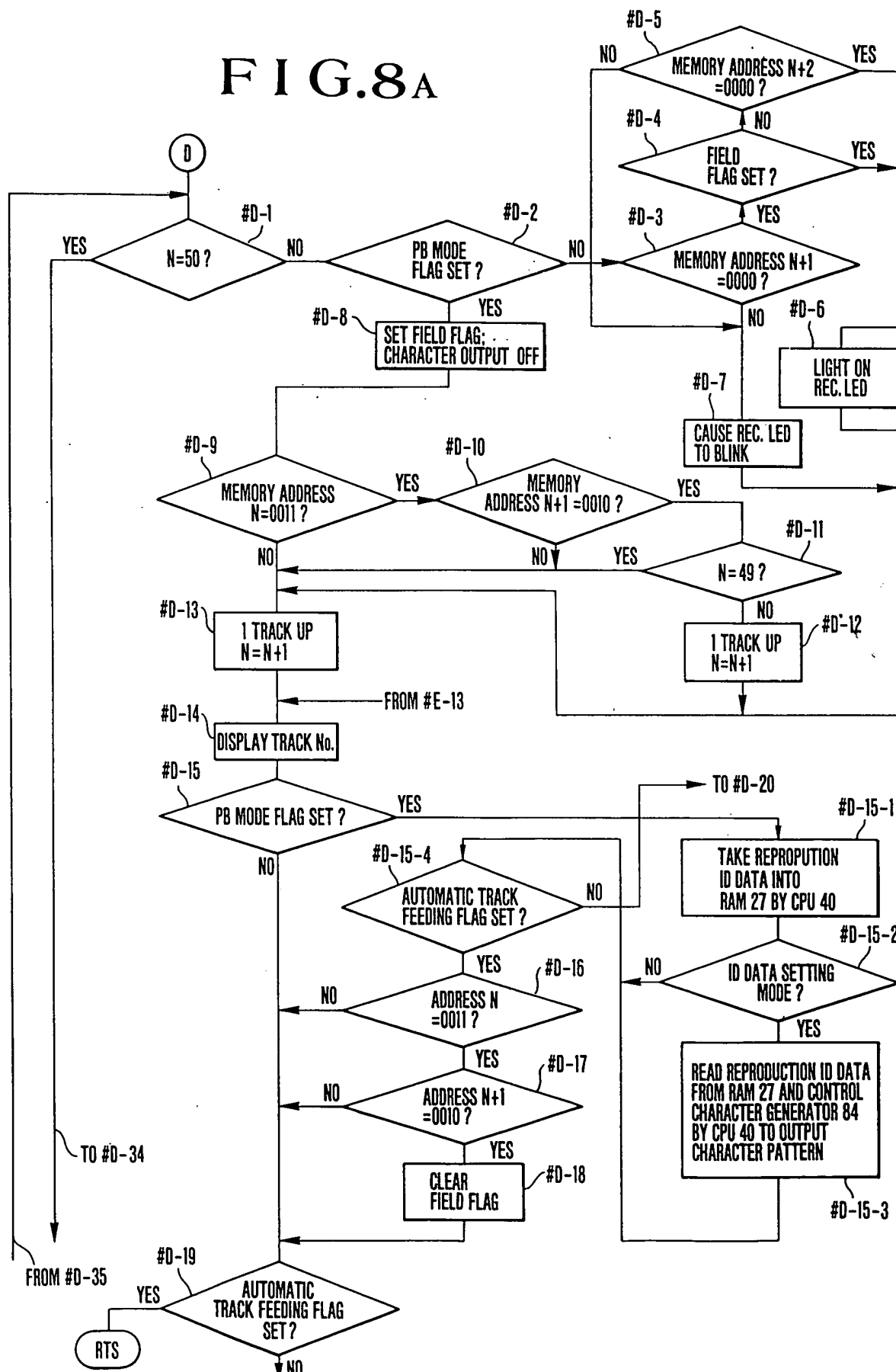
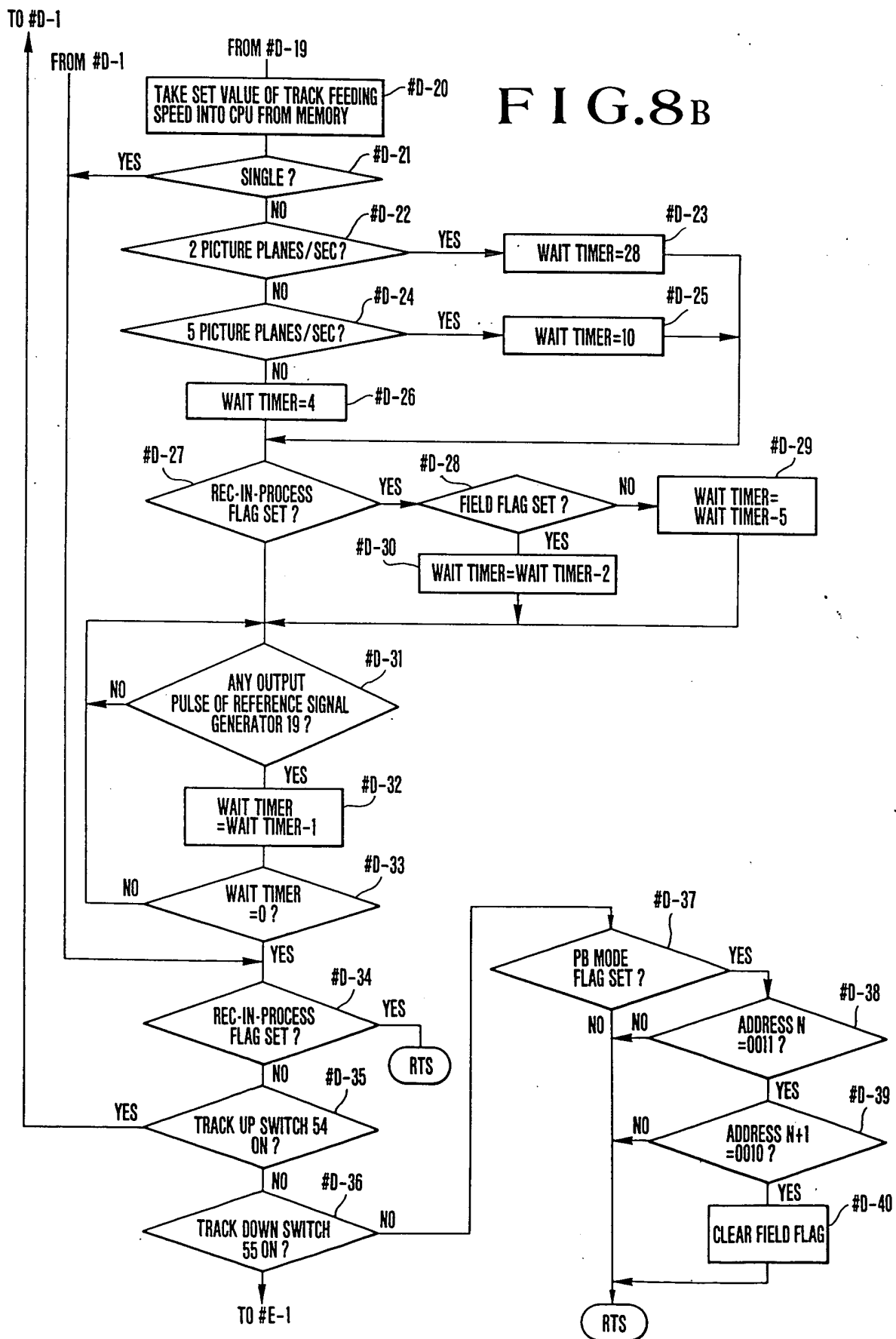


FIG. 7

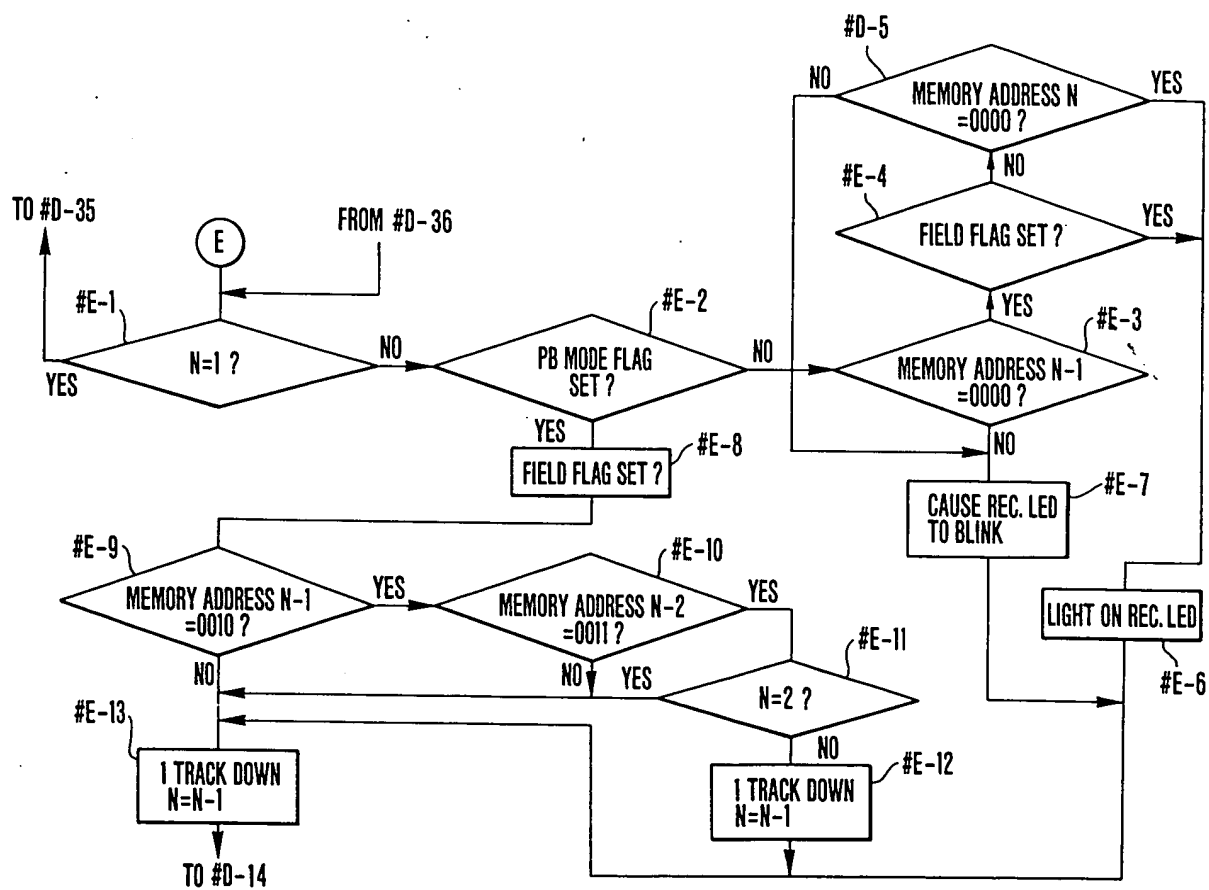




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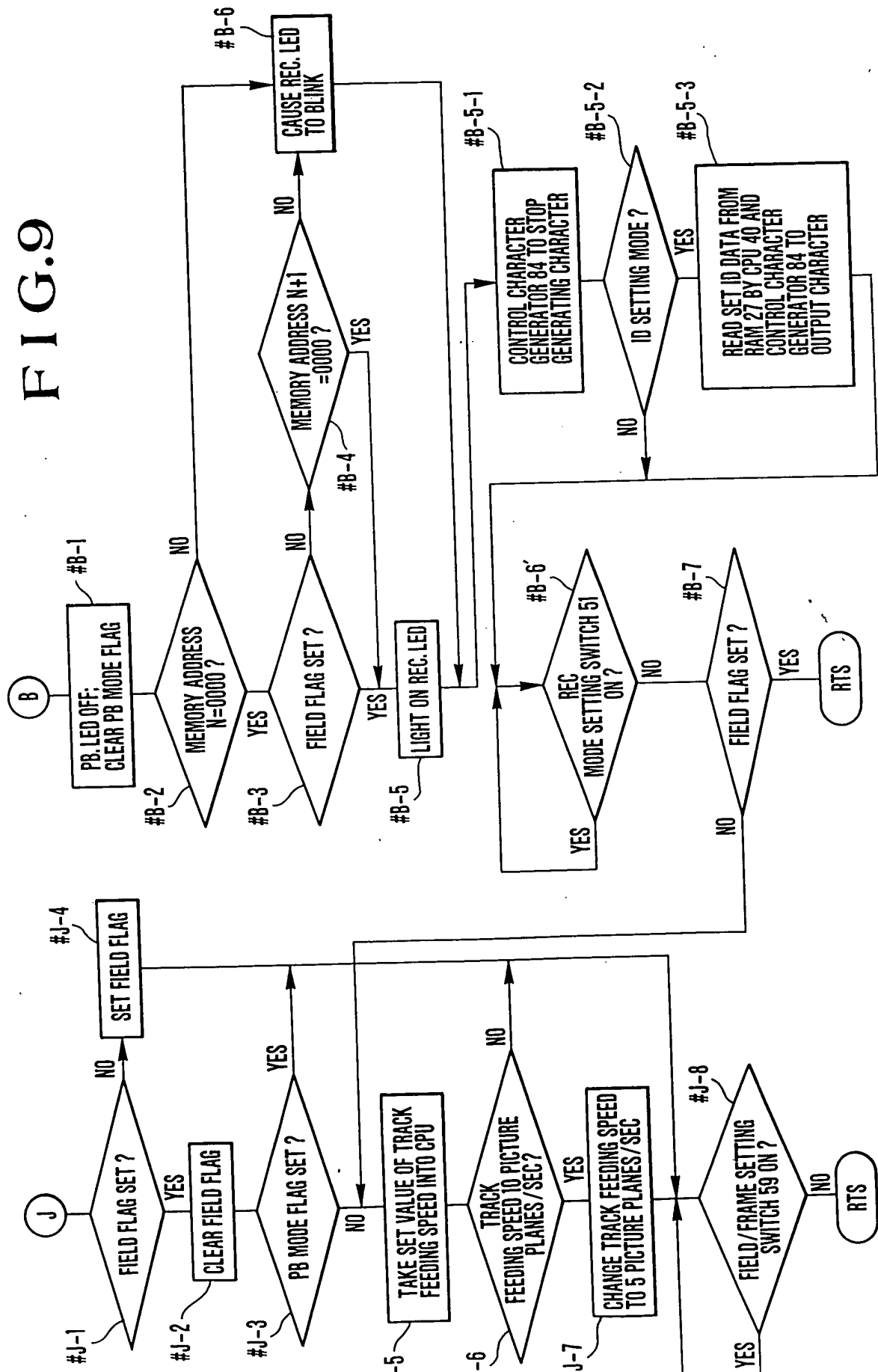


# FIG. 8c



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FIG. 9



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F I G.10

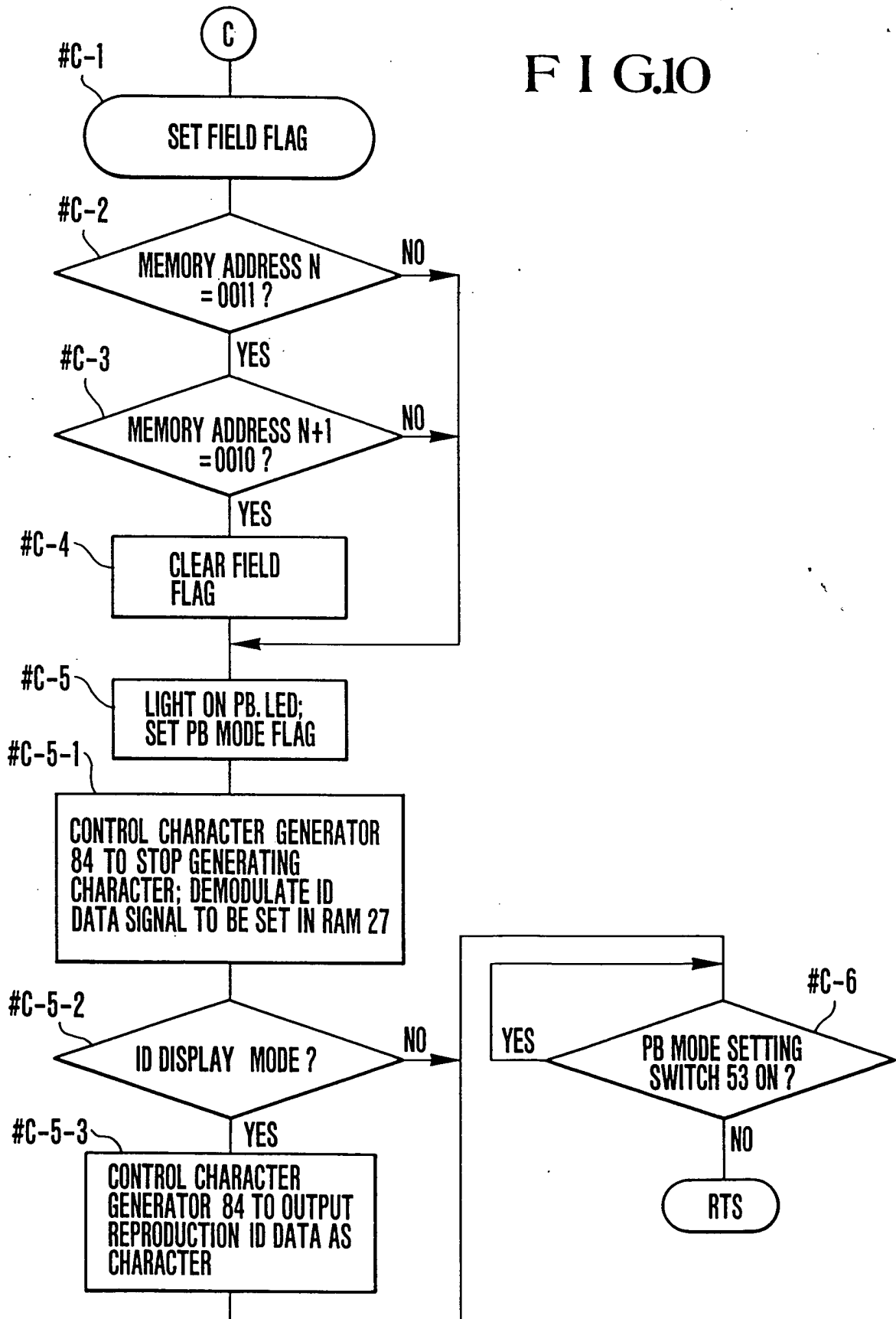
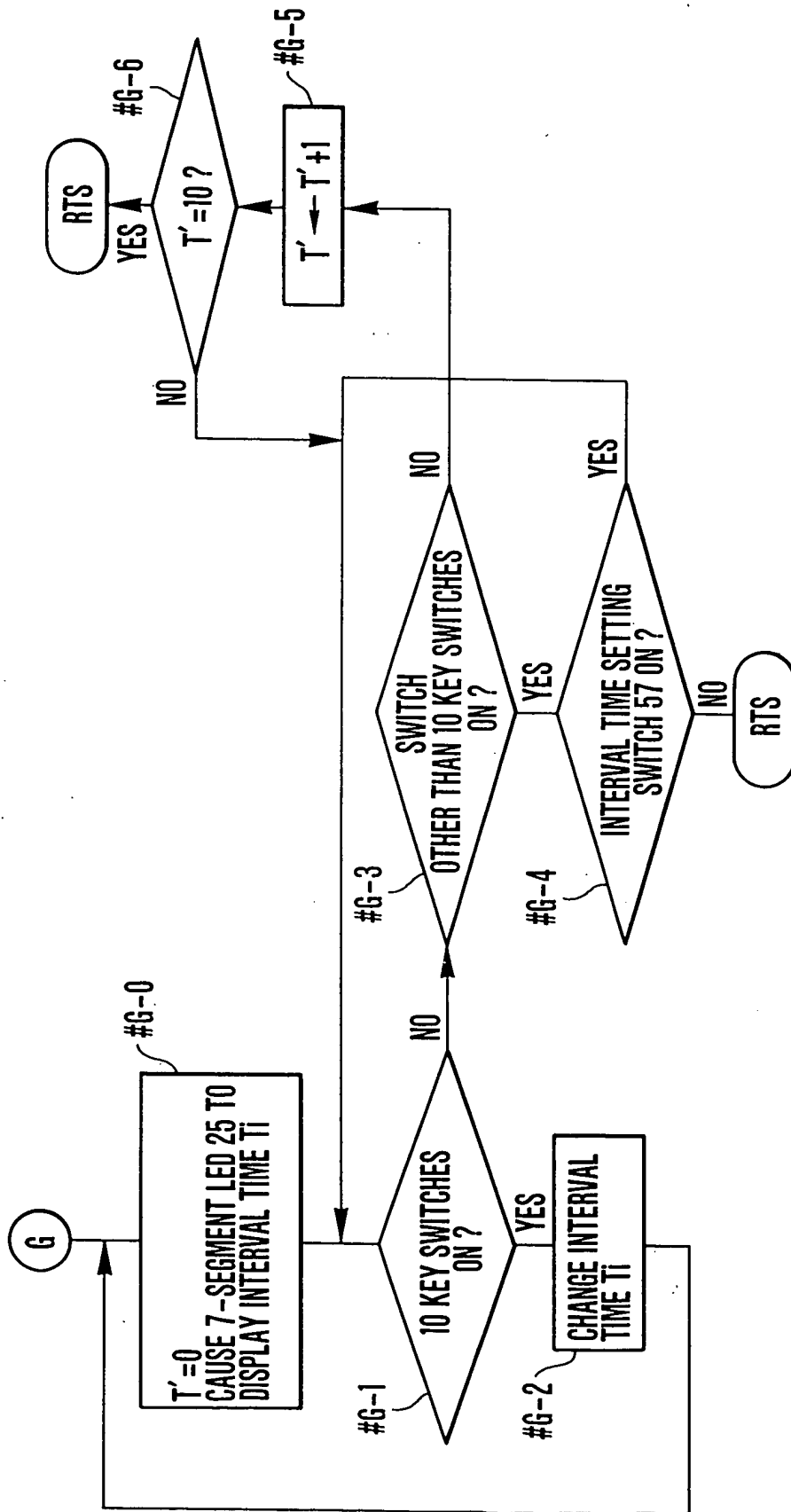


FIG. 11



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# FIG. 12

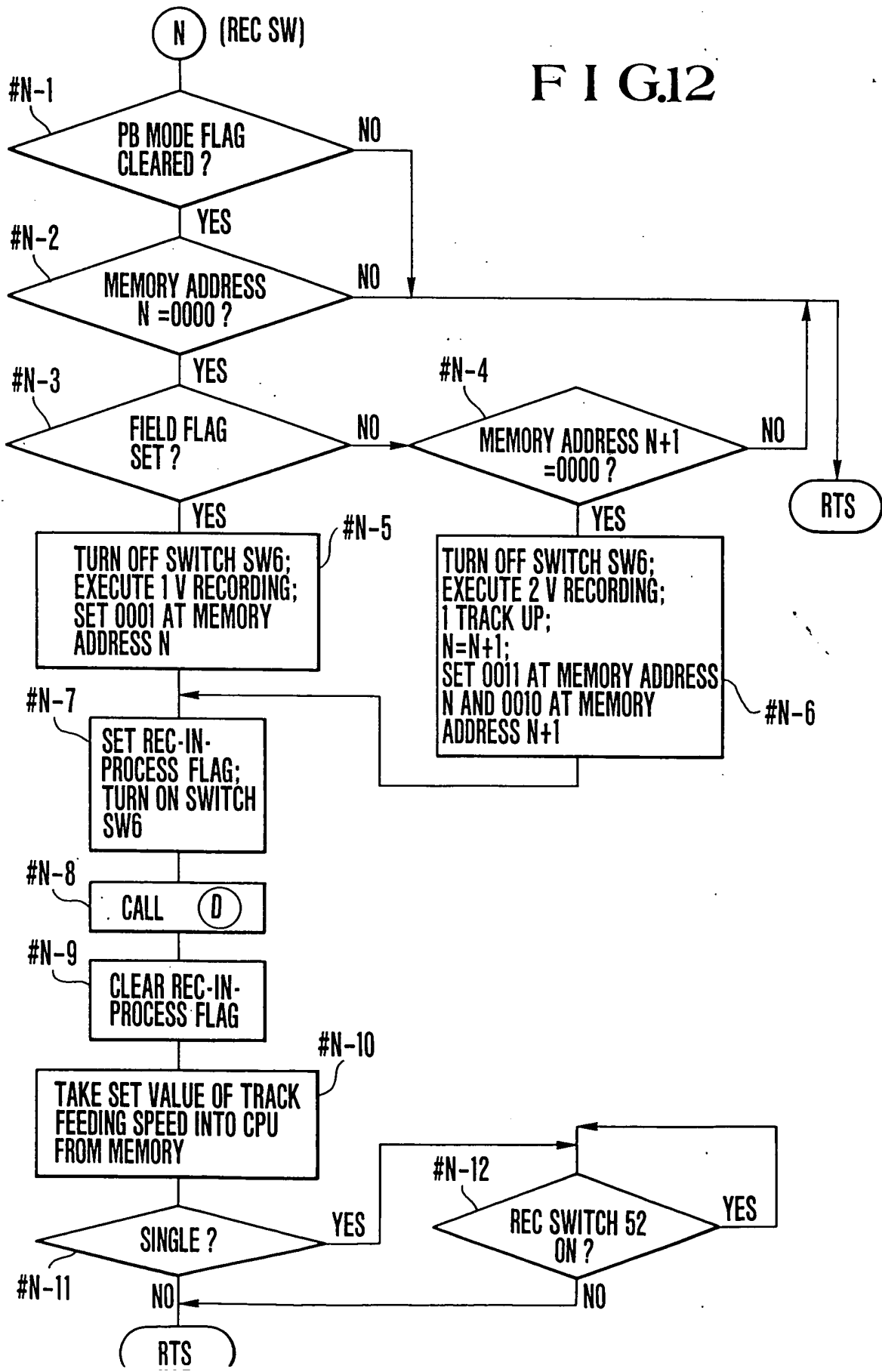


FIG.13

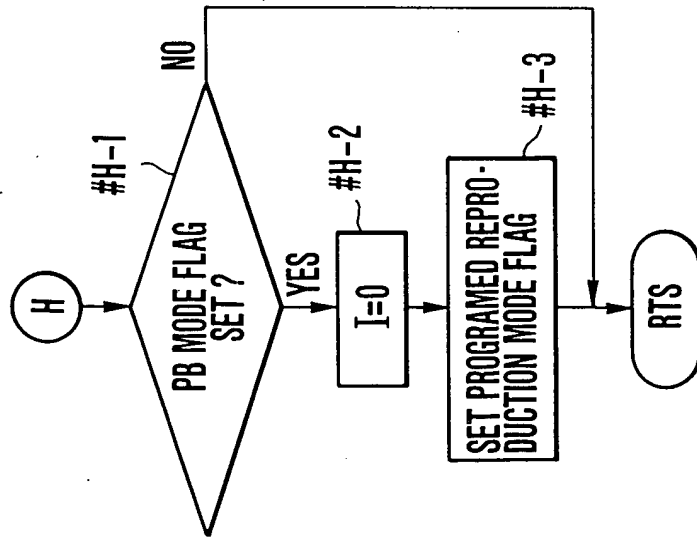


FIG.14

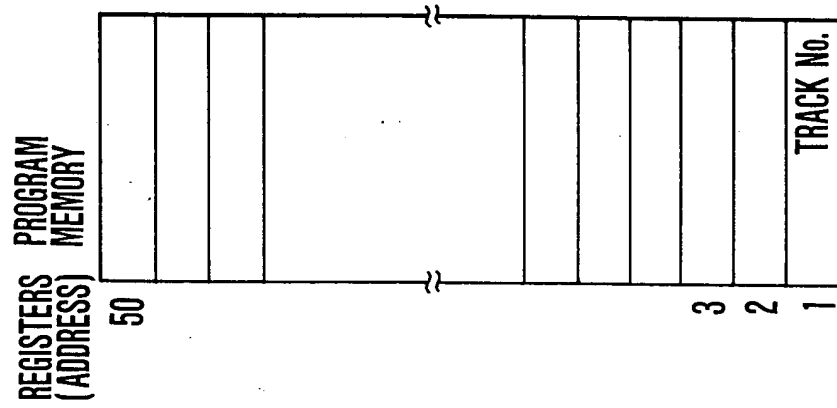




FIG.15

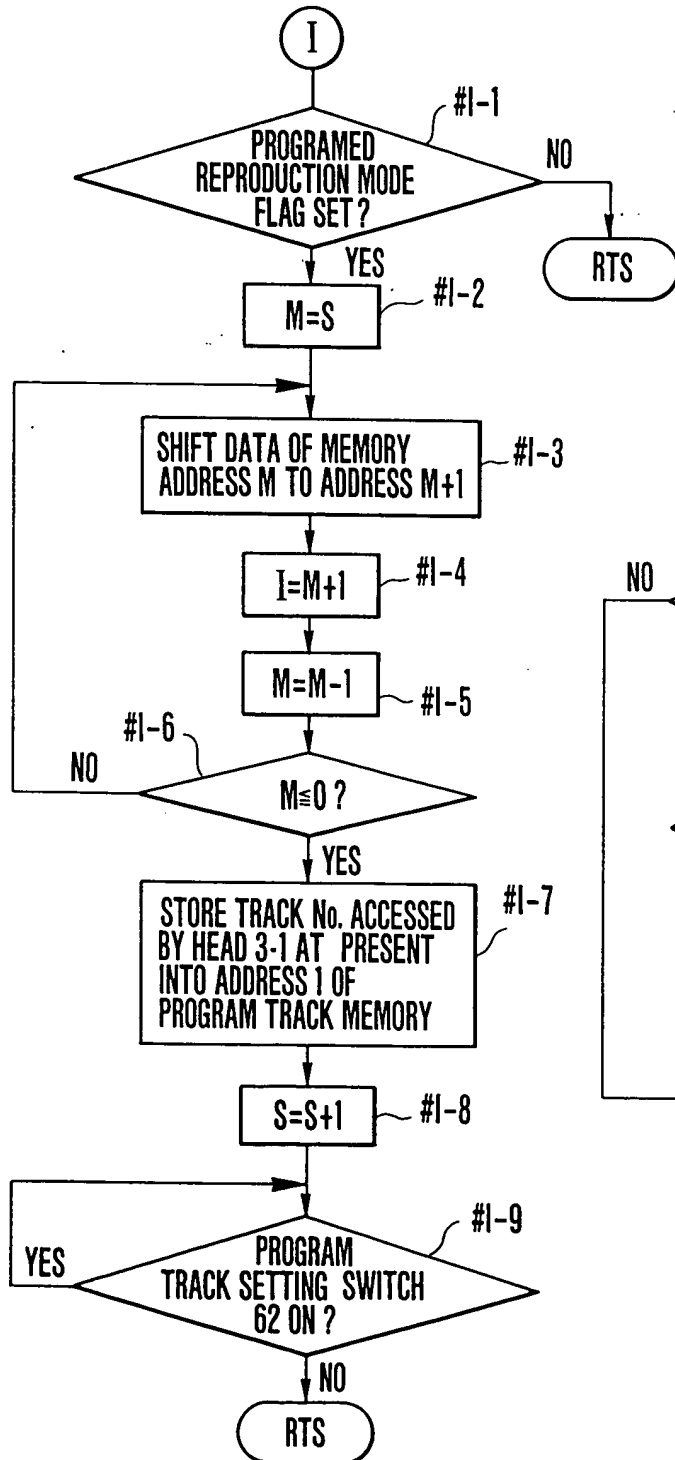


FIG.17

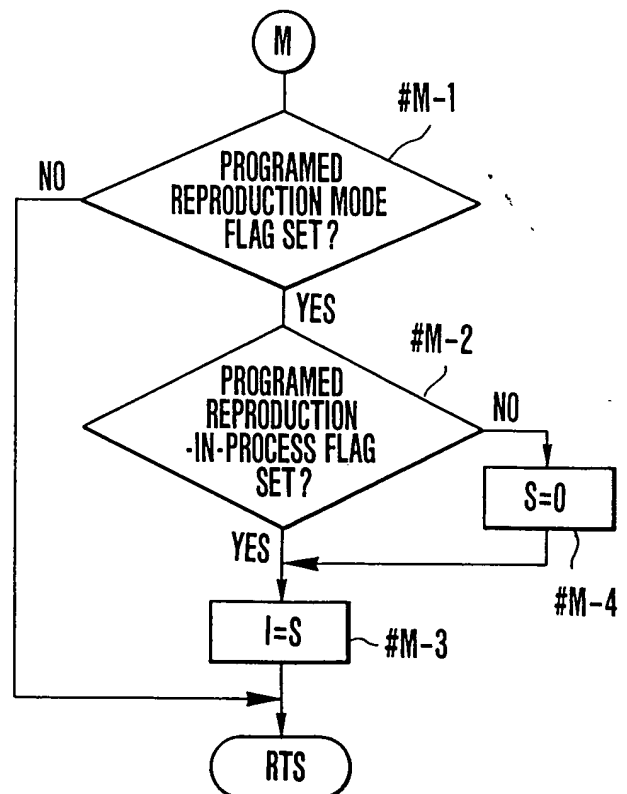
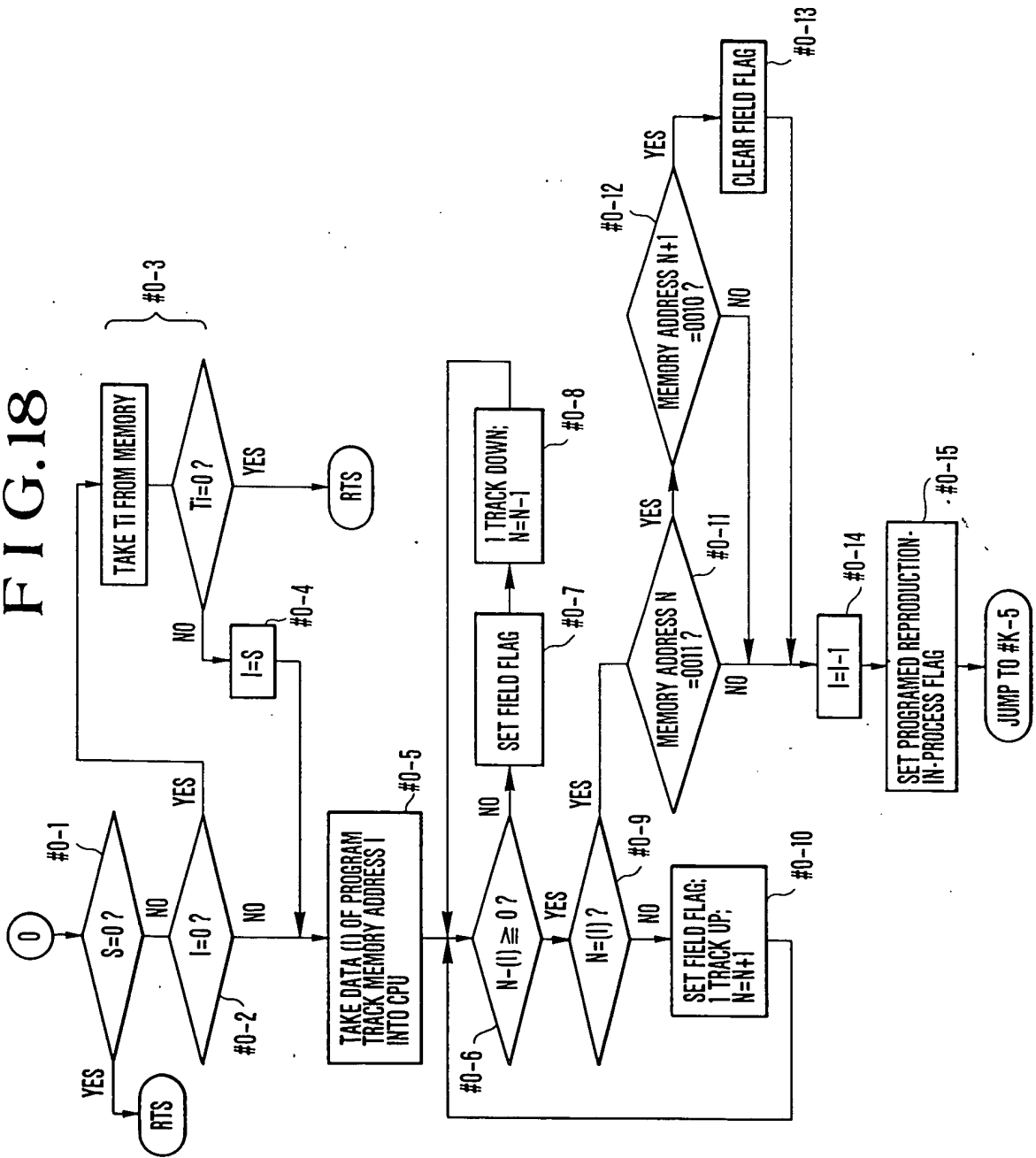




FIG. 18



**F I G.19**

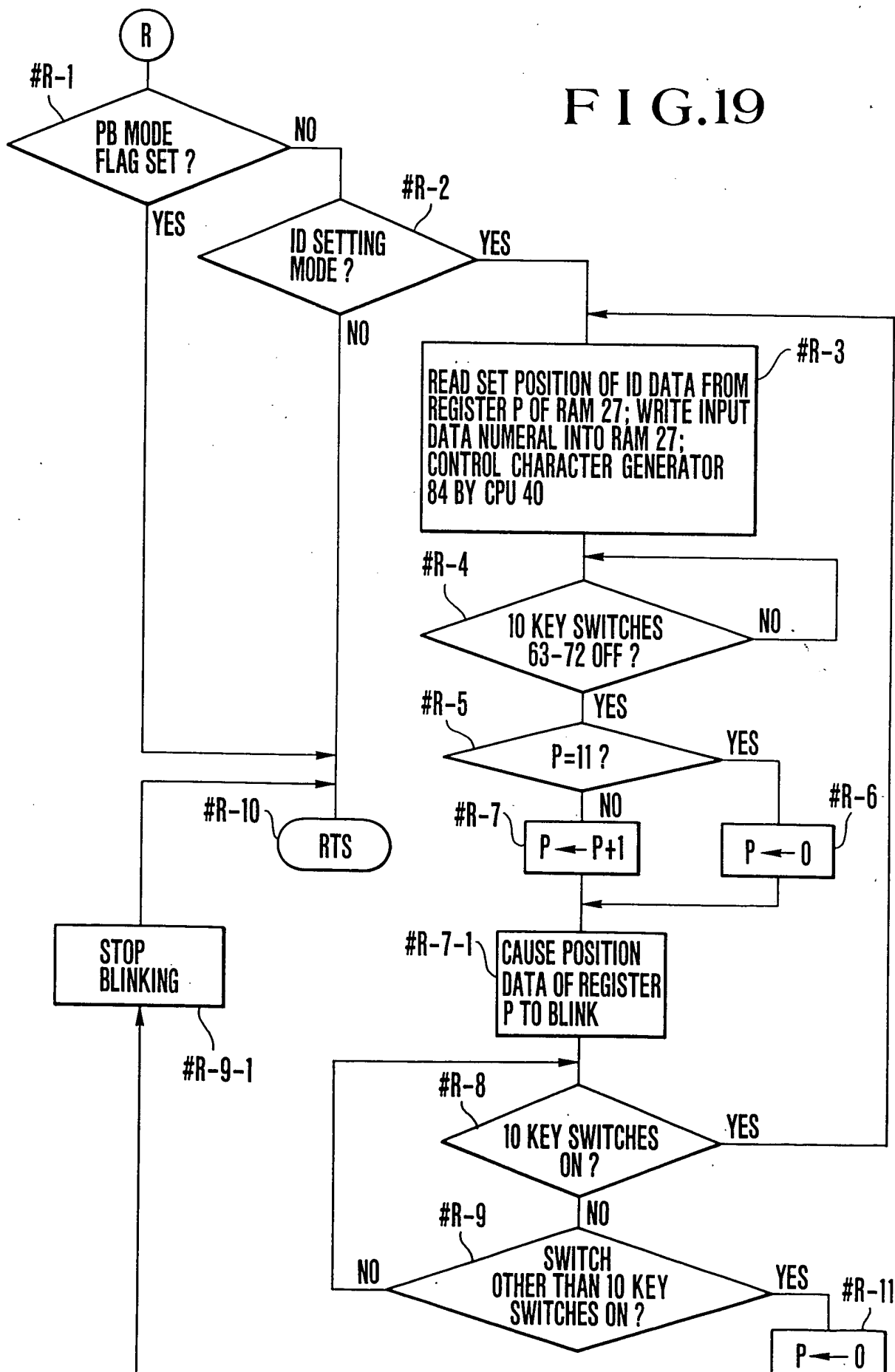
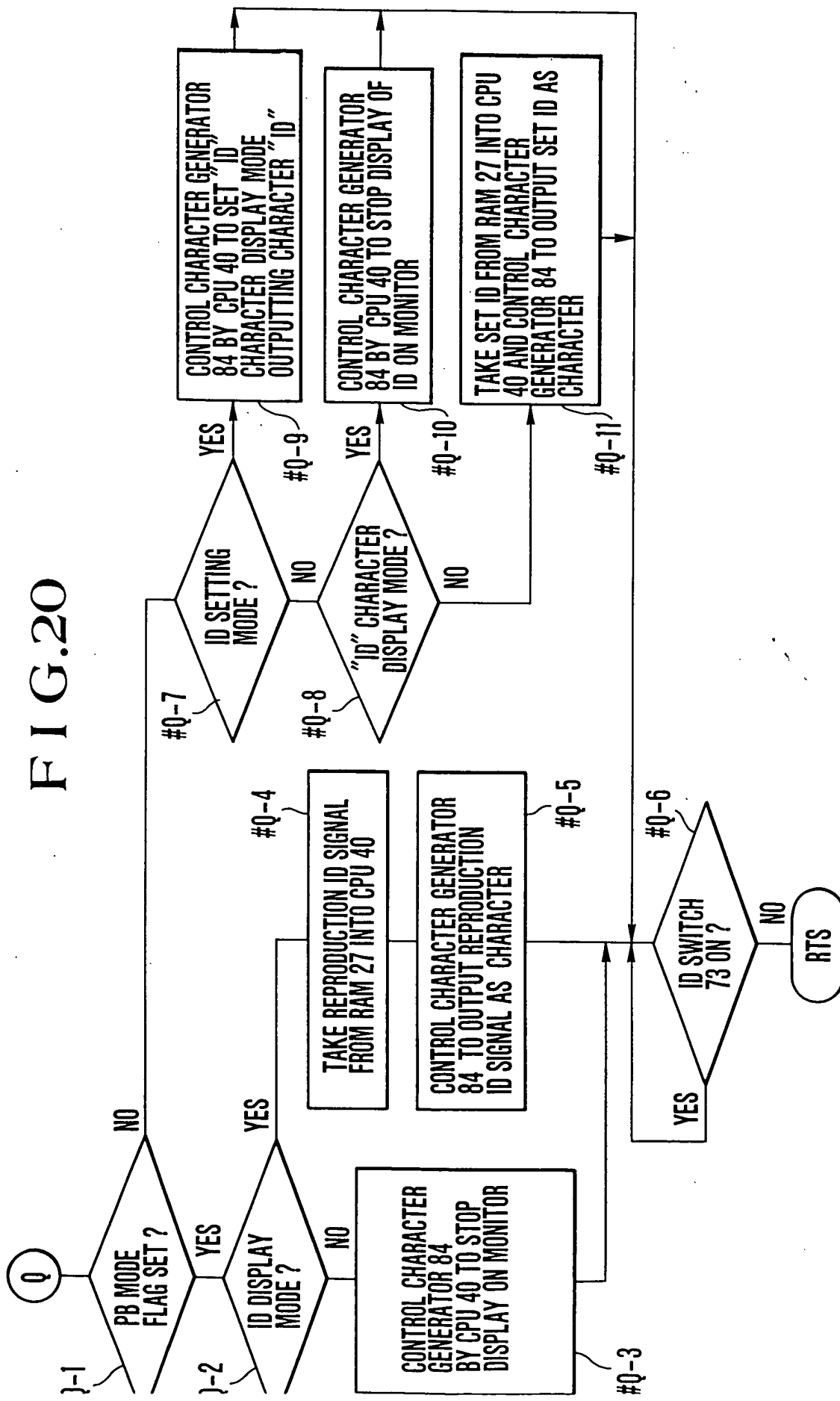


FIG. 20



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FIG.21(a)

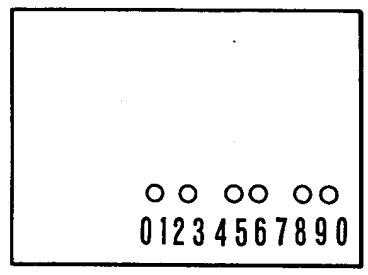


FIG.22(a)

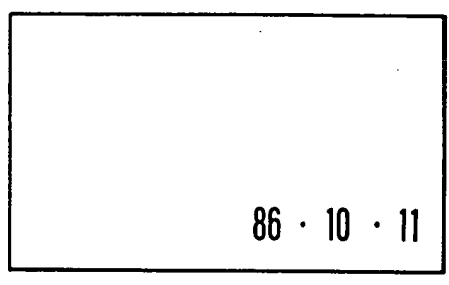


FIG.21(b)

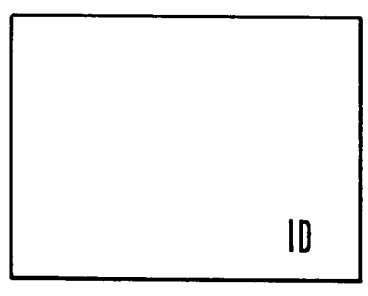


FIG.22(b)

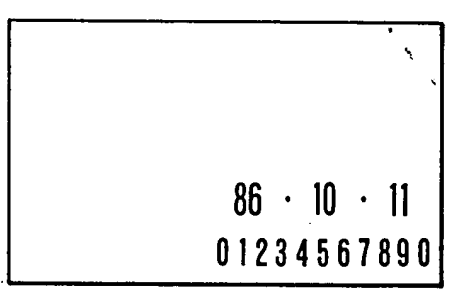


FIG.21(c)

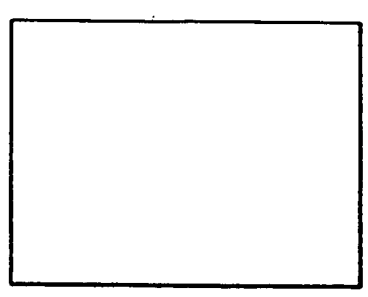


FIG.24

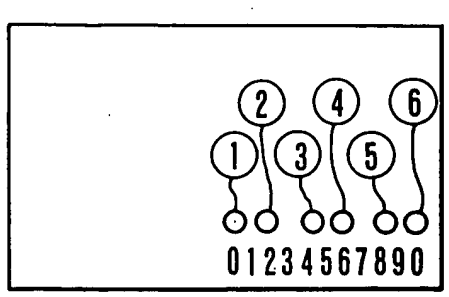


FIG. 23

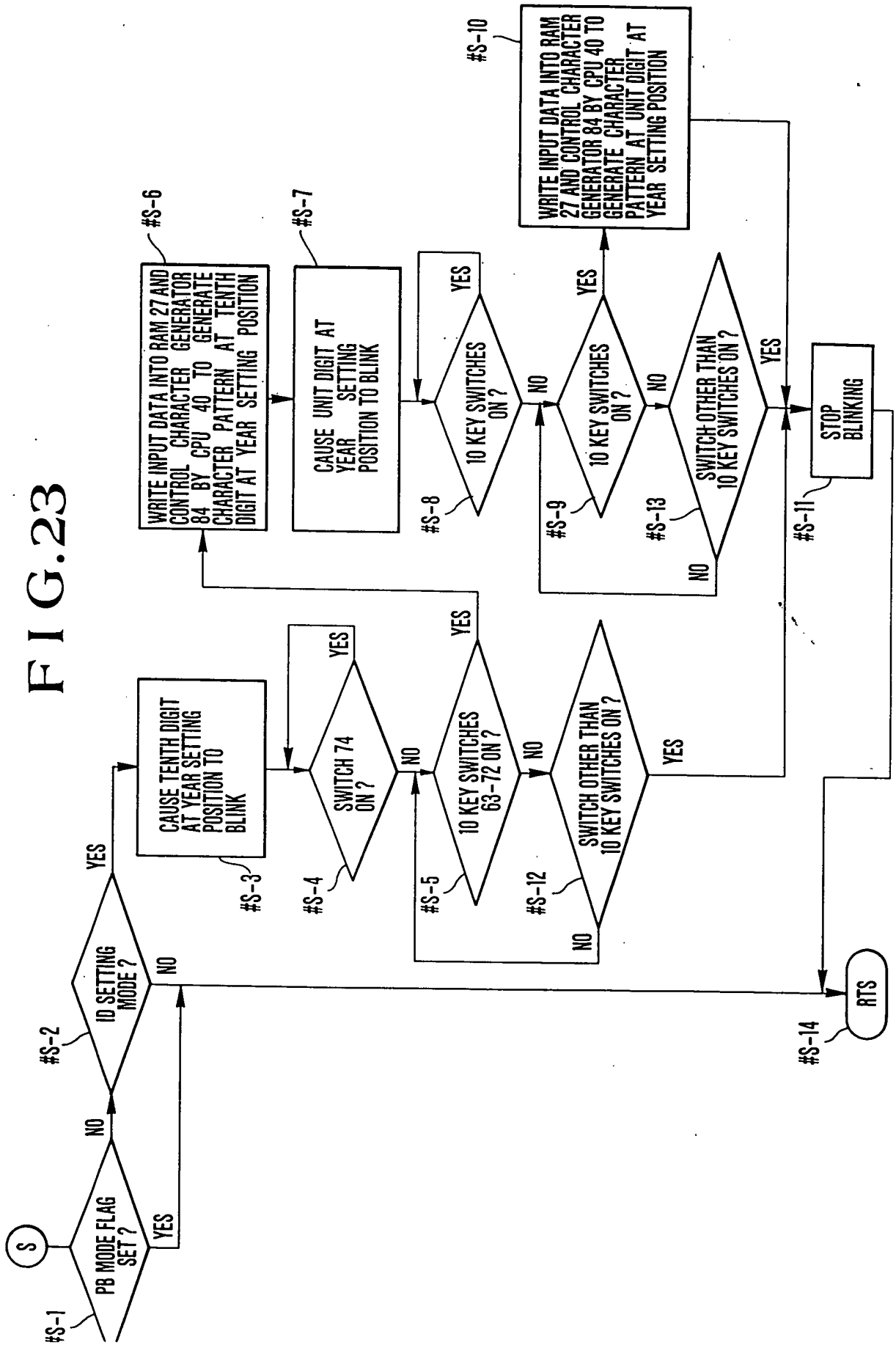
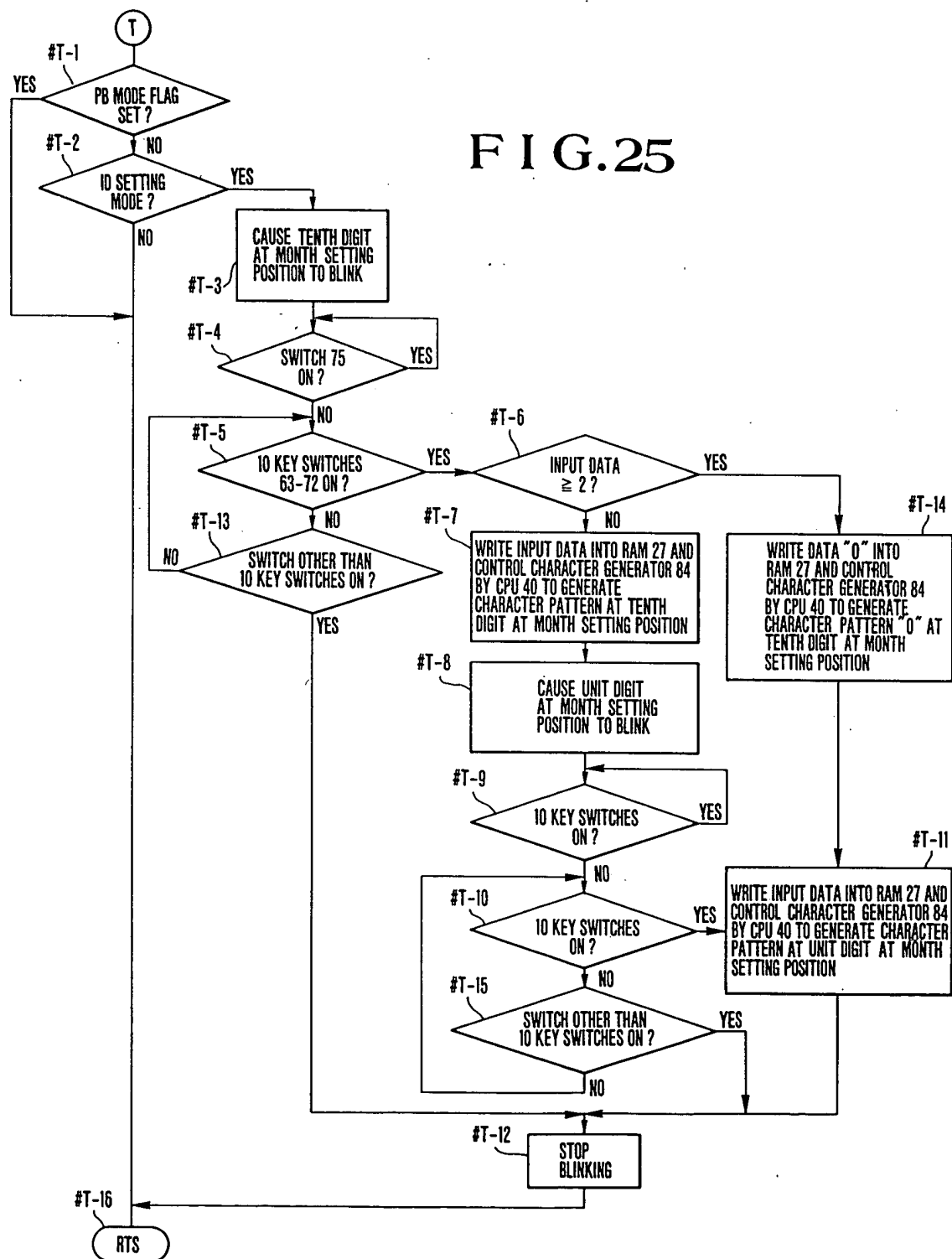


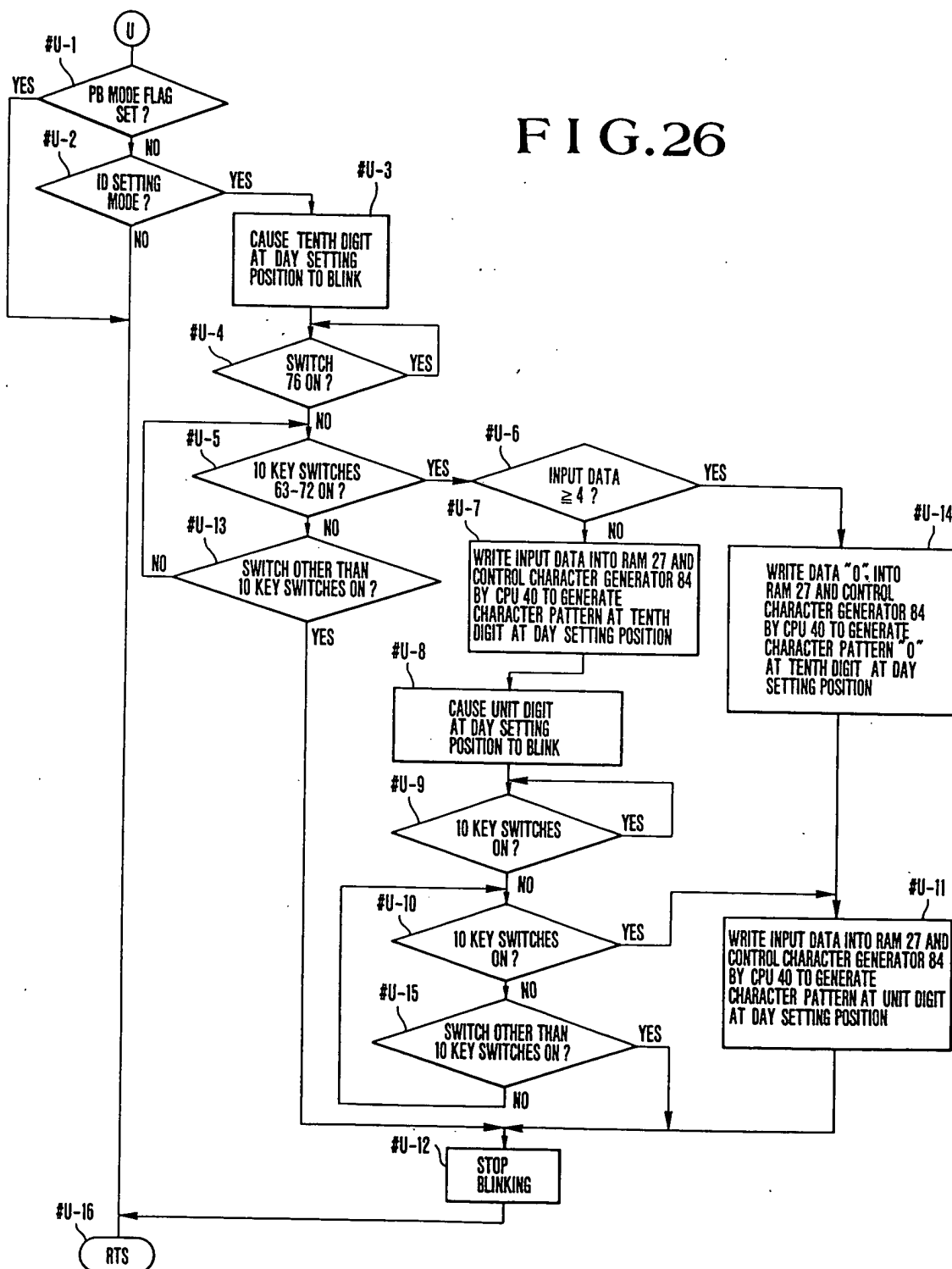
FIG. 25





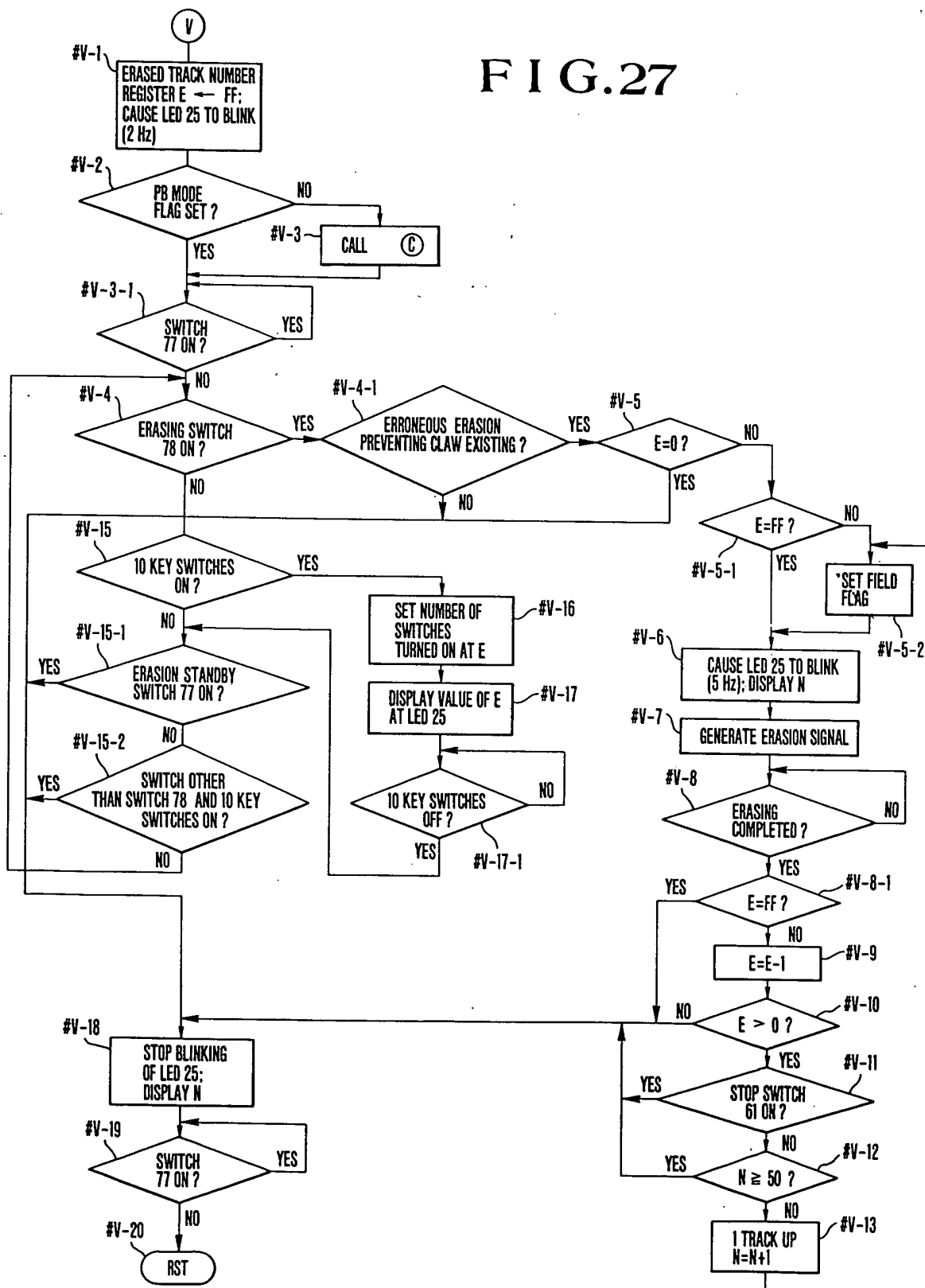
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FIG. 26



**SECRET**

**FIG. 27**



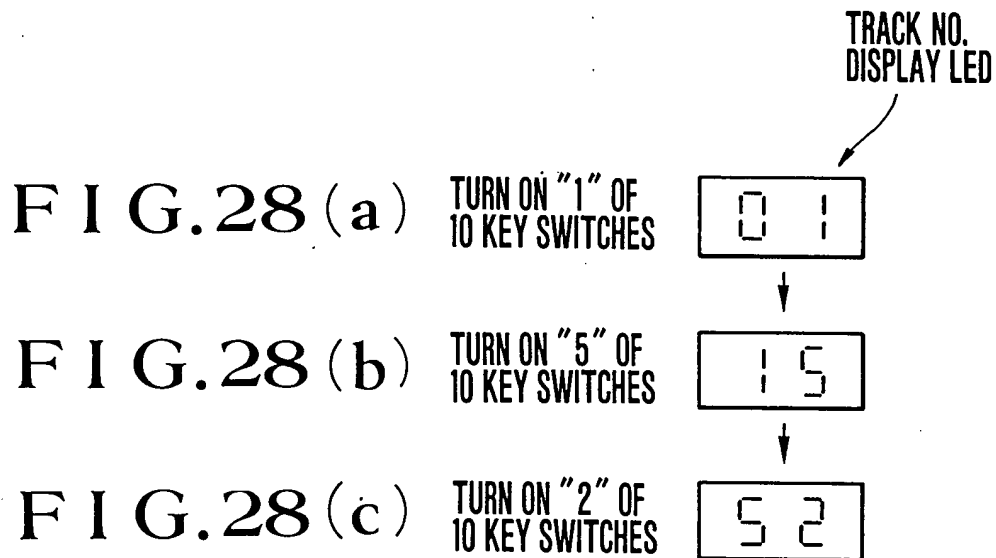


FIG. 32

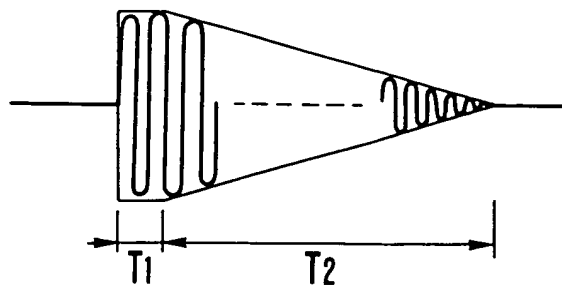


FIG. 29

WHEN RETURNING TO ERASING START POSITION  
 AFTER COMPLETION OF CONTINUOUS ERASING,  
 PROCEED TO THIS FLOW FROM #V-18 (TRACK NO.  
 IF N IS STORED INTO MEMORY N' IN #V-1)

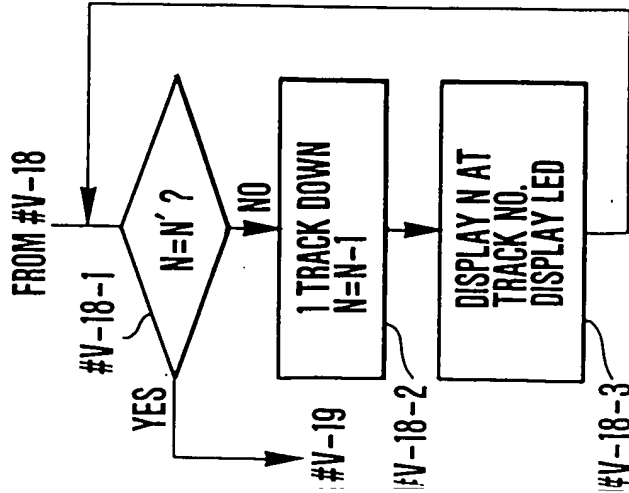


FIG. 30 (a)

CONTINUOUS TRACK ERASING IS EFFECTED  
 ALWAYS IN FRAME MODE IN ORDER TO SPEED UP

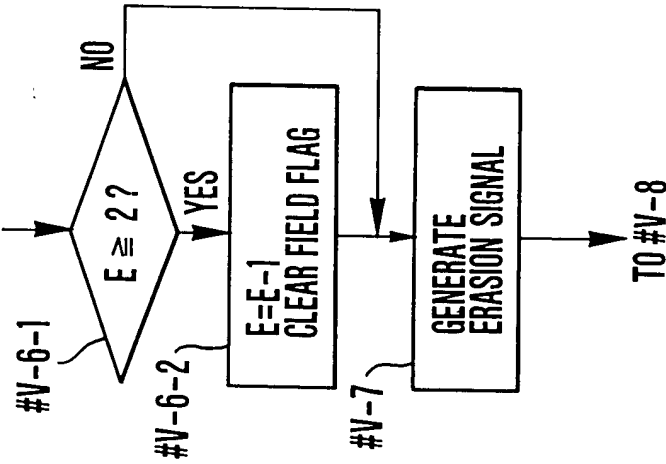
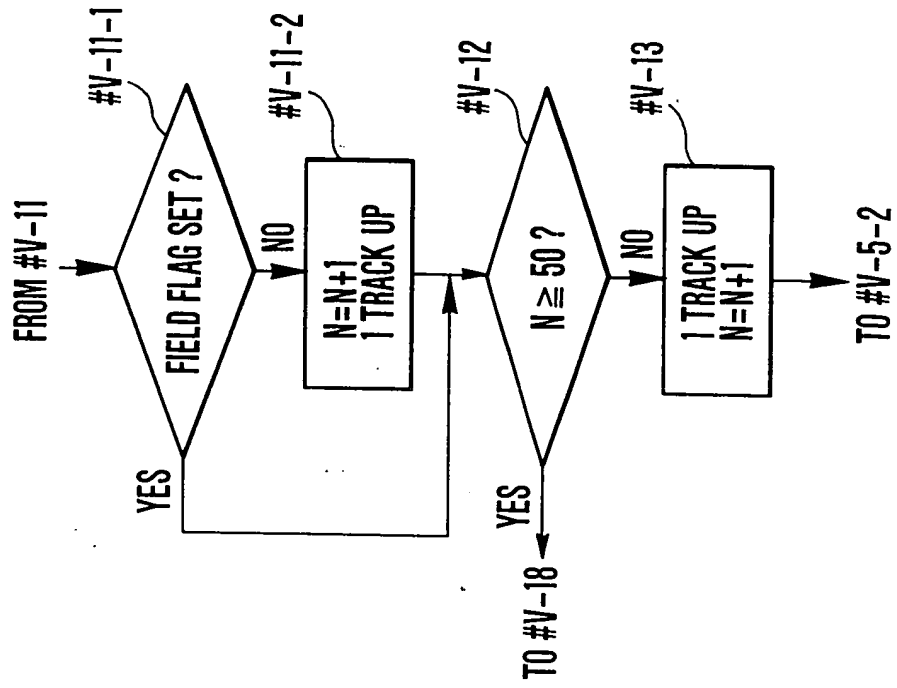


FIG. 30 (b)



**D E F I N I T I O N**

FIG. 31

